

**REMARKS**

Claims 1-24 are pending in the patent application ("Application").

Claims 1-24 have been rejected.

No claims have been allowed.

Claims 1-24 remain in this Application.

Reconsideration of Claims 1-24 is respectfully requested.

**Specification**

The Applicant has amended the specification to correct certain typographical errors.

No new matter has been entered as a result of these amendments.

**Claim Rejections 35 U.S.C. § 102**

In Paragraphs 1-2 on Pages 2-3 of the October 7, 2002 Office Action the Examiner rejected Claims 1, 2, 8, 9, 15 and 20 under 35 U.S.C. § 102(b) as being anticipated by United States Patent No. 6,195,755 to *Webster et al.* (hereafter "*Webster*"). The Applicant respectfully traverses the rejection of Claims 1, 2, 8, 9, 15 and 20.

It is axiomatic that a prior art reference anticipates the claimed invention under 35 U.S.C. § 102 only if every element of a claimed invention is identically shown in that single reference, arranged as they are in the claims. MPEP § 2131; *See, In re King*, 231 U.S.P.Q. 136, 138



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## APPENDIX A

### VERSION WITH MARKINGS TO SHOW CHANGES MADE IN THE SPECIFICATION

**Page 3, Lines 3-9, has been amended as follows:**

In System-On-a-Chip implementations like the Geode™ SC1400 described above, different modules on the chip may be analog and/or digital, according to their designed function. For example, the bridge and the processors are digital modules, while the super input/output (I/O) [modules] module is an analog module. The super input/output (I/O) [modules] module comprises analog elements because it processes analog signals.

**Page 21, Line 24 to Page 22, Line 13, has been amended as follows:**

FIGURE 3 illustrates an exemplary embodiment of a power sense cell 300 for use with the apparatus and method of the present invention. Power sense cells 211, [222] 221, 224, and 231 may each comprise a power sense cell of the type illustrated by power sense cell 300. Power sense cell technology is well known. Various implementations of power sense cells may be adequate for use in the present invention. Power sense cell 300 comprises a Schmitt trigger circuit 304. Schmitt trigger circuit 304 is a bistable circuit in which a transition from the output state from a High level to a Low level occurs at a lower output voltage than the input voltage required for a transition of the output state from a Low level to a High level. This feature is indicated by the hysteresis loop shown on the Schmitt trigger symbol in FIGURE 3.

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